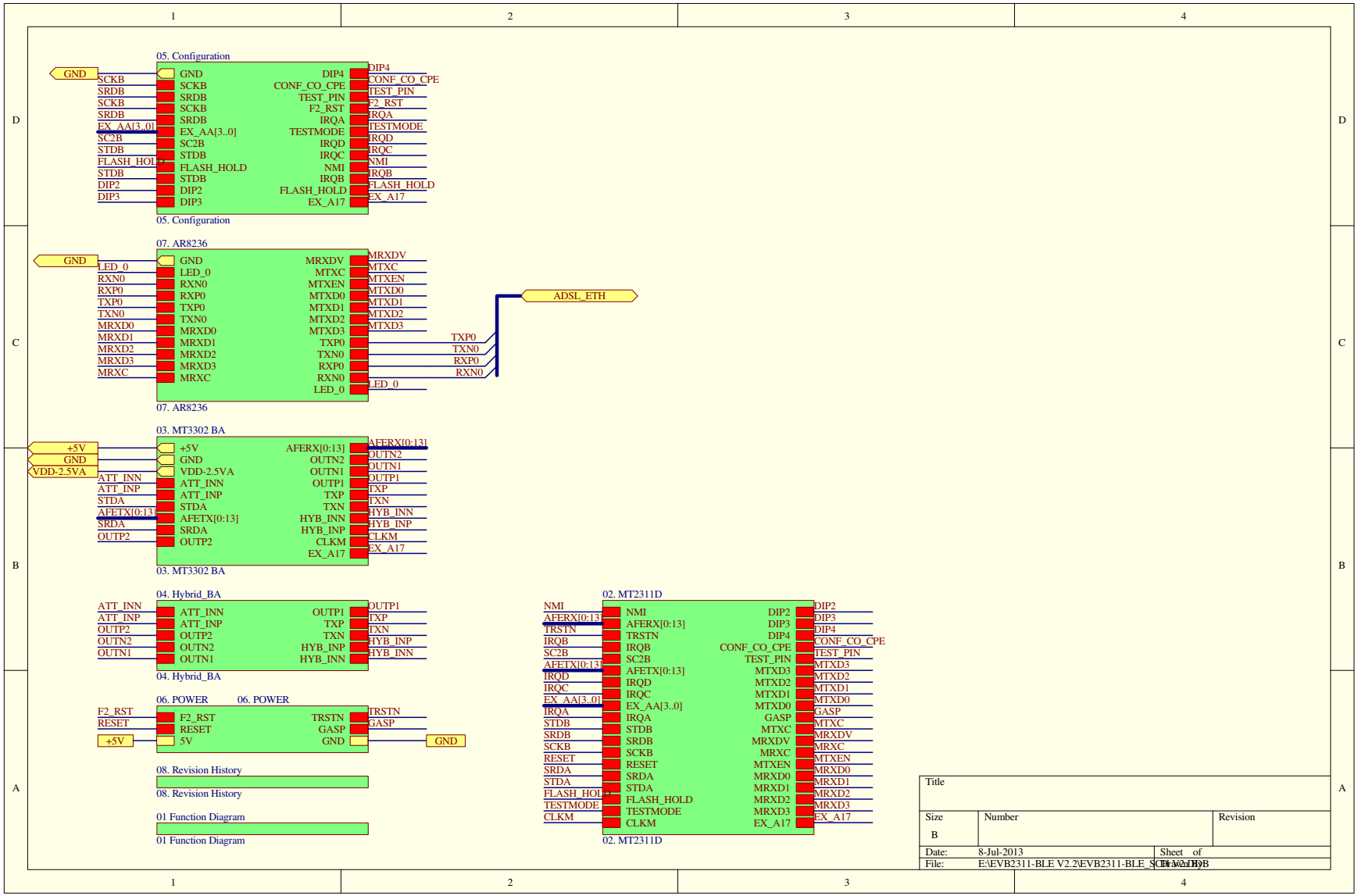
05. Configuration
GND
SCKB
SRDB
SCKB
SRDB
EX_AA[3..0]
SC2B
STDB
FLASH_HOLD
STDB
DIP2
DIP3
GND
SCKB
SRDB
SCKB
SRDB
EX_AA[3..0]
SC2B
STDB
FLASH_HOLD
STDB
DIP2
DIP3
DIP4
CONF_CO_CPE
TEST_PIN
F2_RST
IRQA
TESTMODE
IRQD
IRQC
NMI
IRQB
FLASH_HOLD
EX_A17
DIP4
CONF_CO_CPE
TEST_PIN
F2_RST
IRQA
TESTMODE
IRQD
IRQC
NMI
IRQB
FLASH_HOLD
EX_A17
05. Configuration
07. AR8236
GND
LED_0
RXN0
RXP0
TXP0
TXN0
MRXD0
MRXD1
MRXD2
MRXD3
MRXC
GND
LED_0
RXN0
RXP0
TXP0
TXN0
MRXD0
MRXD1
MRXD2
MRXD3
MRXC
MRXDV
MTXC
MTXEN
MTXD0
MTXD1
MTXD2
MTXD3
TXP0
TXN0
RXP0
RXN0
LED_0
MRXDV
MTXC
MTXEN
MTXD0
MTXD1
MTXD2
MTXD3
TXP0
TXN0
RXP0
RXN0
LED_0
ADSL_ETH
07. AR8236
03. MT3302 BA
+5V
GND
VDD-2.5VA
ATT_INN
ATT_INP
STDA
AFETX[0:13]
SRDA
OUTP2
+5V
GND
VDD-2.5VA
ATT_INN
ATT_INP
STDA
AFETX[0:13]
SRDA
OUTP2
AFERX[0:13]
OUTN2
OUTN1
OUTP1
TXP
TXN
HYB_INN
HYB_INP
CLKM
EX_A17
AFERX[0:13]
OUTN2
OUTN1
OUTP1
TXP
TXN
HYB_INN
HYB_INP
CLKM
EX_A17
03. MT3302 BA
04. Hybrid_BA
ATT_INN
ATT_INP
OUTP2
OUTN2
OUTN1
ATT_INN
ATT_INP
OUTP2
OUTN2
OUTN1
OUTP1
TXP
TXN
HYB_INP
HYB_INN
OUTP1
TXP
TXN
HYB_INP
HYB_INN
04. Hybrid_BA
06. POWER
06. POWER
F2_RST
RESET
+5V
F2_RST
RESET
5V
TRSTN
GASP
GND
TRSTN
GASP
GND
08. Revision History
08. Revision History
01 Function Diagram
01 Function Diagram
02. MT2311D
NMI
AFERX[0:13]
TRSTN
IRQB
SC2B
AFETX[0:13]
IRQD
IRQC
EX_AA[3..0]
IRQA
STDB
SRDB
SCKB
RESET
SRDA
STDA
FLASH_HOLD
TESTMODE
CLKM
NMI
AFERX[0:13]
TRSTN
IRQB
SC2B
AFETX[0:13]
IRQD
IRQC
EX_AA[3..0]
IRQA
STDB
SRDB
SCKB
RESET
SRDA
STDA
FLASH_HOLD
TESTMODE
CLKM
DIP2
DIP3
DIP4
CONF_CO_CPE
TEST_PIN
MTXD3
MTXD2
MTXD1
MTXD0
GASP
MTXC
MRXDV
MRXC
MTXEN
MRXD0
MRXD1
MRXD2
MRXD3
EX_A17
DIP2
DIP3
DIP4
CONF_CO_CPE
TEST_PIN
MTXD3
MTXD2
MTXD1
MTXD0
GASP
MTXC
MRXDV
MRXC
MTXEN
MRXD0
MRXD1
MRXD2
MRXD3
EX_A17
02. MT2311D
Title
Size
B
Number
Revision
Date: 8-Jul-2013
Sheet of
File: E:\EVB2311-BLE V2.2\EVB2311-BLE_SCH.DDB
Drawn by: