

Support Utility User Manual

Version: 1.2

Installation:

Before installation, please make sure you download the latest version of driver/firmware manager/dslMonitor.

1. Hardware installation

Connect USB controller to PC and reference board as figure 1, make sure the cable direction is correct. If the direction is not correct, utility will not work.

2. Driver installation

When PC connect to USB controller, driver installation is requirement. Please select the driver in FTDI_USB_driver

Version X3 DIP:

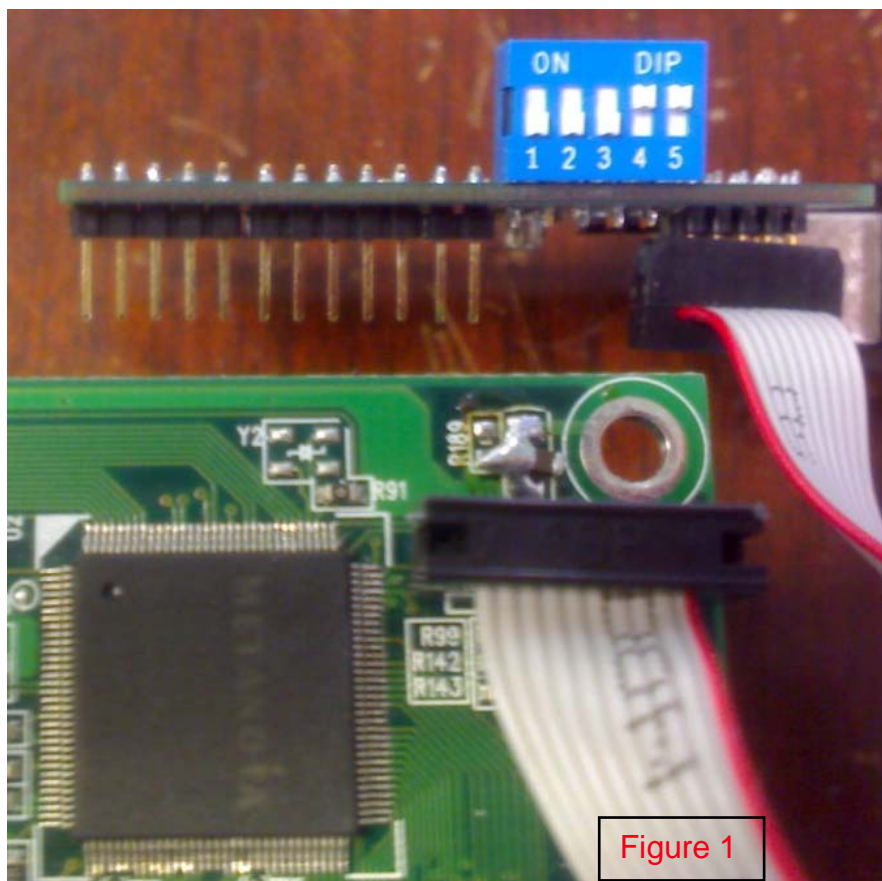
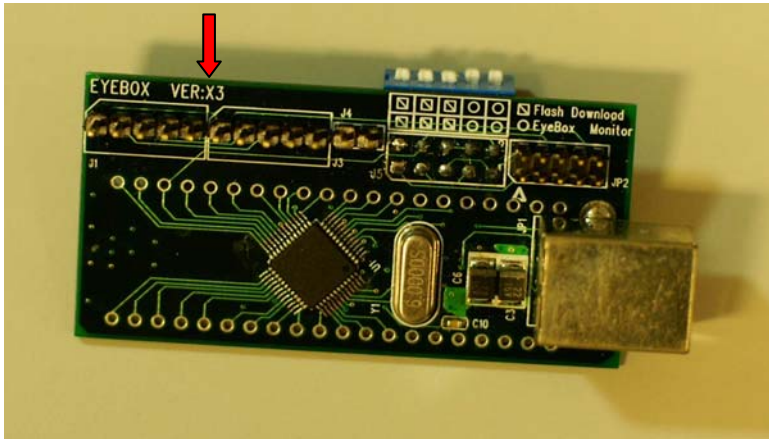
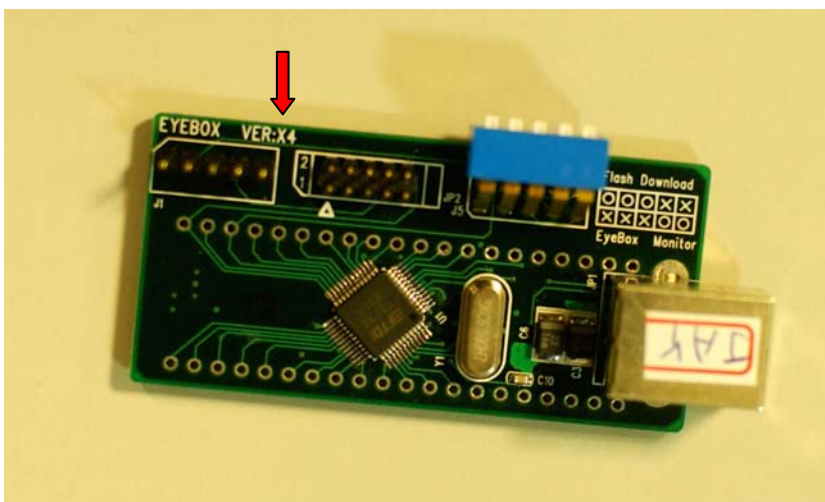
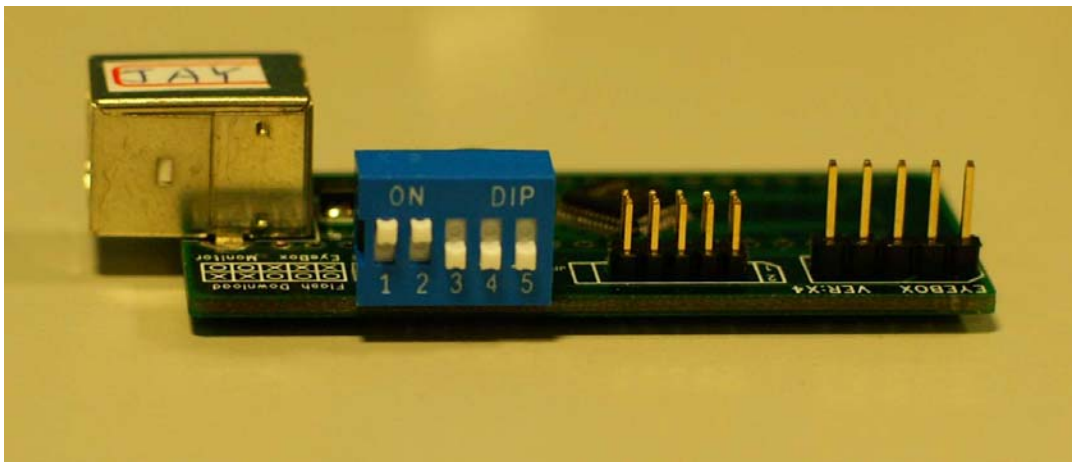


Figure 1

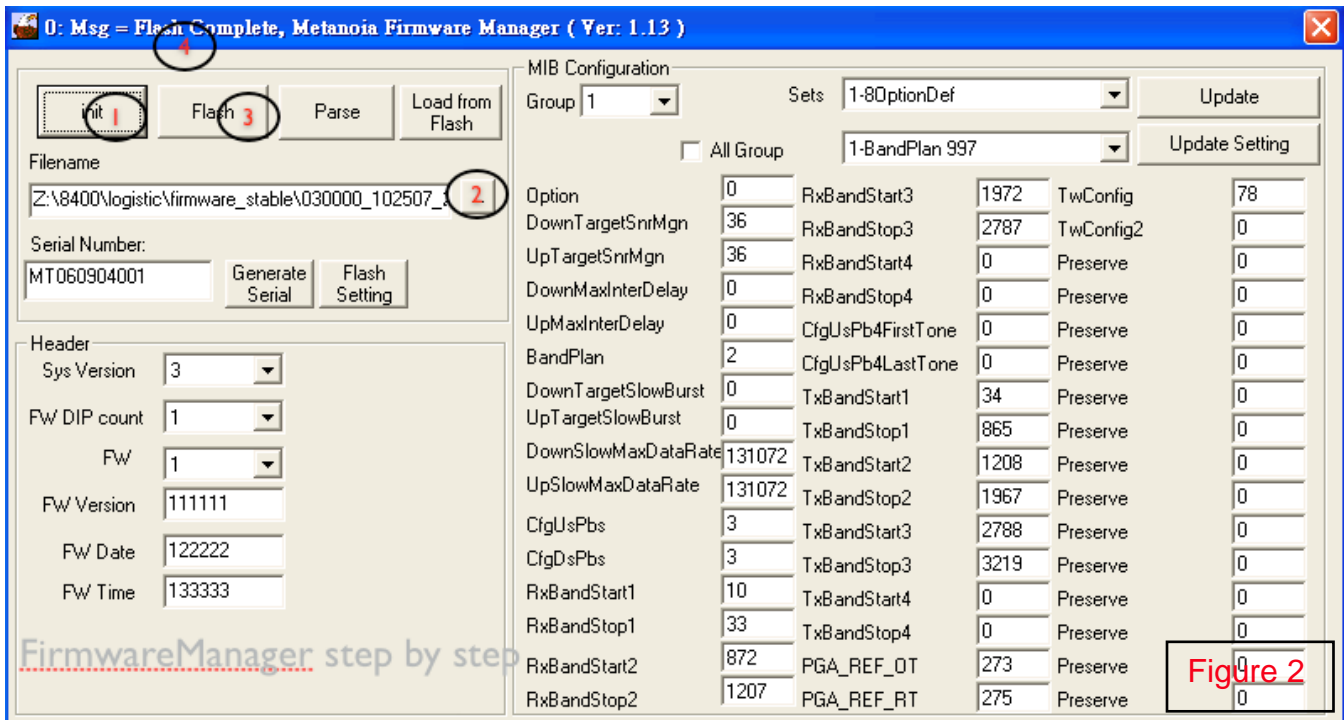


Version X4 DIP:



Using FirmwareManager

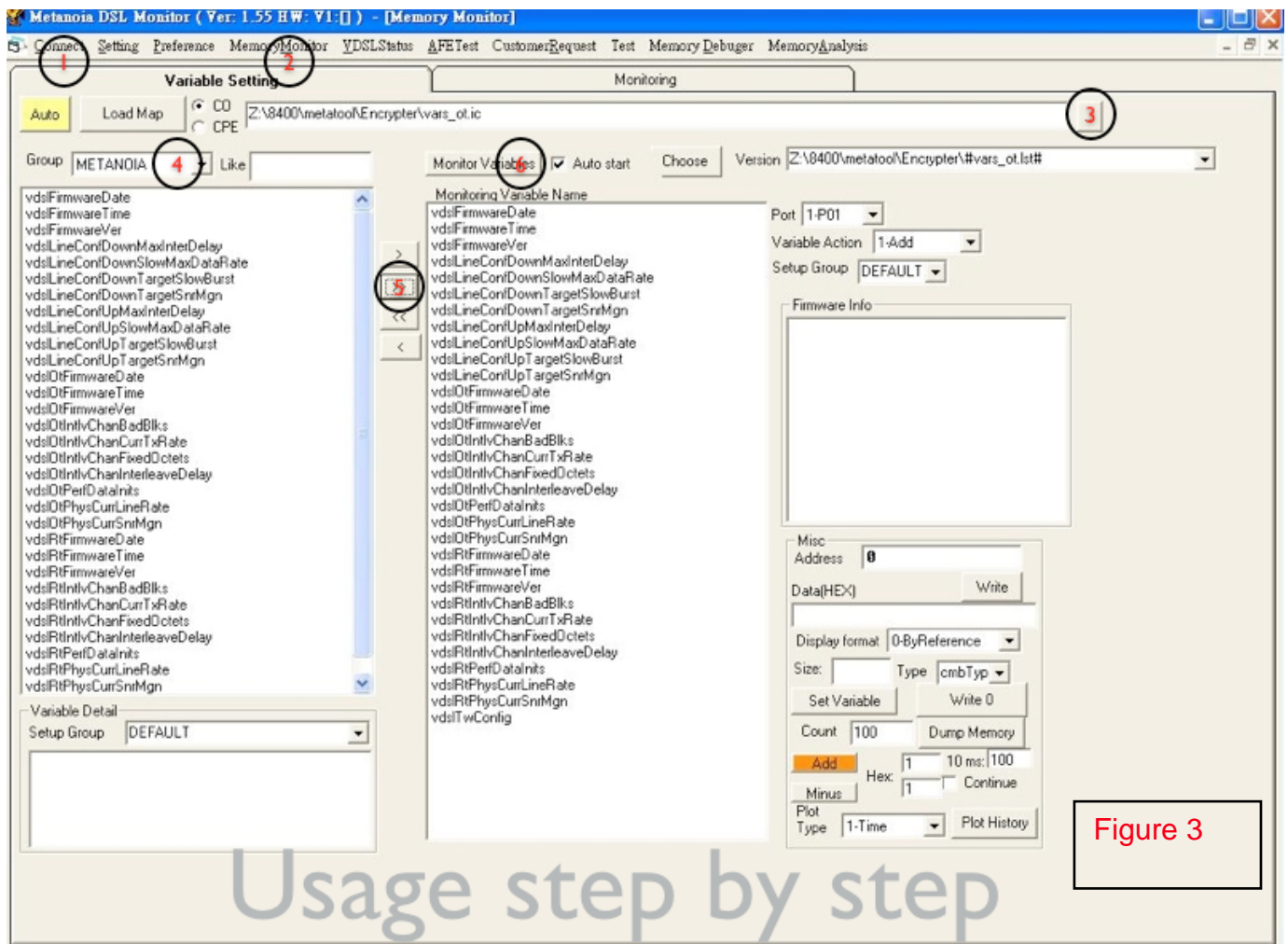
1. Make sure eyebox controller DIP setting is
X3: 1,2,3 at ON and 4,5 at OFF
X4: 3,4,5 at ON and 1,2 at OFF
2. Run the FirmwareManager.exe in the directory
3. Follow instruction of figure 2 to flash firmware



4. After about 1 min, you will see "Flash complete" in the window caption.

Using DslMonitor

1. Make sure eyebox controller DIP setting is
X3: 1,2,3 at OFF and 4,5 at ON
X4: 3,4,5 at OFF and 1,2 at ON
2. Follow the instruction of figure 3 to monitor modem status

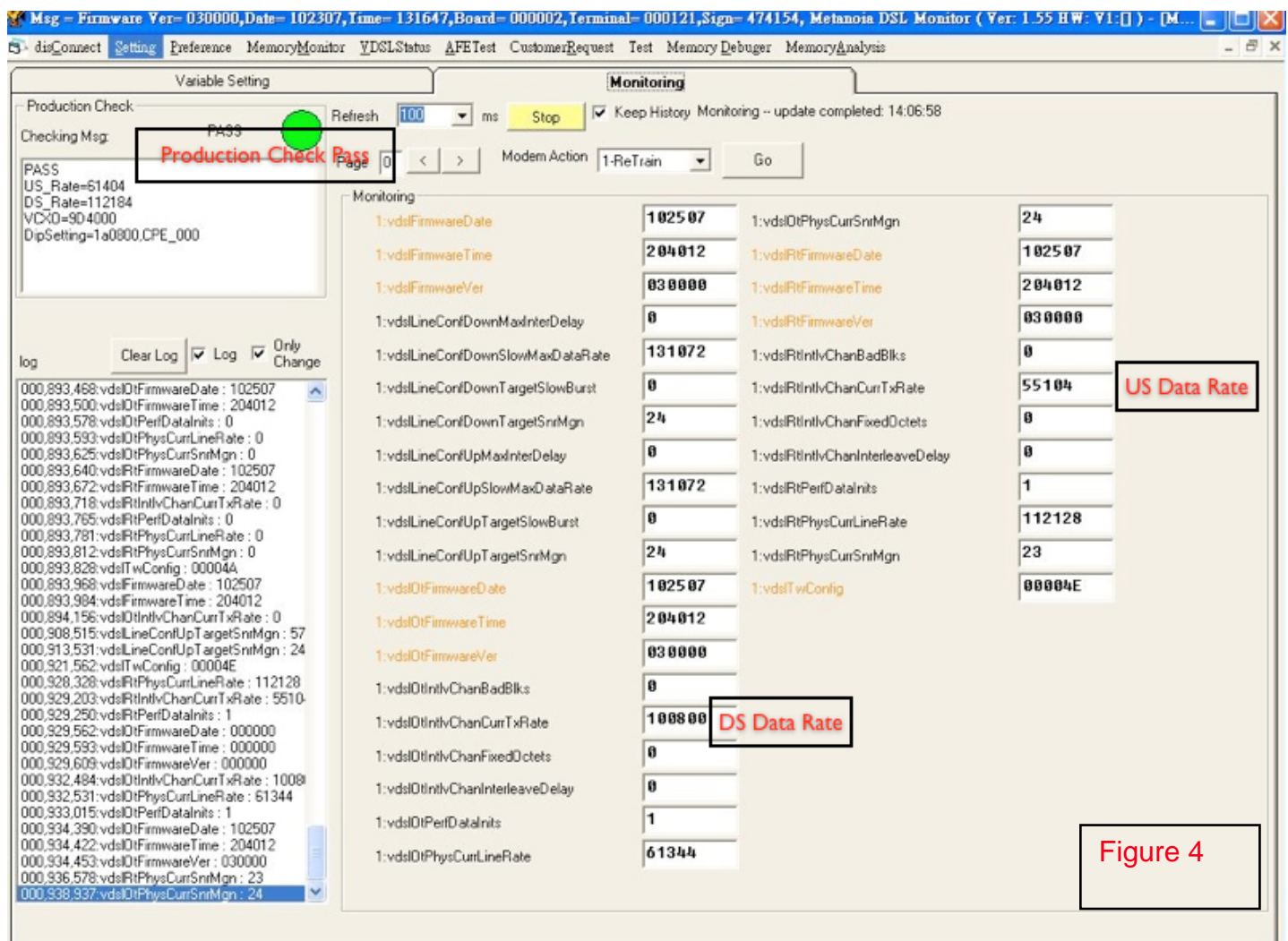


2.1 After step 1, firmware information will be show up on window caption, you must select the correct ic file of same firmware version. CO(OT)/CPE(RT) must select correctly.If terminal is xxxxx1, this is CPE(RT). If terminal is xxxxx0, this is

CO(OT)

3. The result as figure 4

4 Variable definition in table



2. Header variables description

Variable Name	Default Value	Units
ml_DownloadRate	PL (Mbit/s)	Data rate (LTM 64 kbps)
ml_UplinkRate	PL (Mbit/s)	Data rate (LTM 64 kbps)
vhdlrChannelRate	PL (Mbit/s)	Channel Rate
vhdlrChannelSnr	PL (Mbit/s)	Channel SNR
vhdlrChannelWd	PL (Mbit/s)	Channel width
vhdlrConnDownMaxNetDelay	PL (Mbit/s)	DownStream Max network delay (ms)
vhdlrConnDownMaxNetDelayL	PL (Mbit/s)	DownStream Rate Limit
vhdlrConnDownMaxNetDelayH	PL (Mbit/s)	DownStream BWP (us)
vhdlrConnDownMaxSnrMgn	PL (Mbit/s)	large DownStream SNR Mgn
vhdlrConnUpMaxNetDelay	PL (Mbit/s)	UpStream Max network delay (ms)
vhdlrConnUpMaxNetDelayL	PL (Mbit/s)	UpStream Rate Limit
vhdlrConnUpMaxNetDelayH	PL (Mbit/s)	UpStream BWP (us)
vhdlrConnUpMaxSnrMgn	PL (Mbit/s)	large UpStream SNR Mgn
vhdlrCrcVChnUseBbIs	PL (Mbit/s)	CRC counts
vhdlrCrcVChnUseCbIs	PL (Mbit/s)	crc correct byte
vhdlrCrcVChnUseCbIs	PL (Mbit/s)	connection success counter
vhdlrCrcVChnUseCbIs	PL (Mbit/s)	Current UpStream Inside
vhdlrCrcVChnUseCbIs	PL (Mbit/s)	Current UpStream SNR Mgn
vhdlrCrcVChnUseCbIs	PL (Mbit/s)	CRC counts
vhdlrCrcVChnUseCbIs	PL (Mbit/s)	crc correct byte
vhdlrCrcVChnUseCbIs	PL (Mbit/s)	connection success counter
vhdlrCrcVChnUseCbIs	PL (Mbit/s)	Current DownStream Inside
vhdlrCrcVChnUseCbIs	PL (Mbit/s)	Current DownStream SNR Mgn
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	Select debug bit
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	DownStream and 0 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	DownStream and 0 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	DownStream and 1 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	DownStream and 1 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	DownStream and 2 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	DownStream and 2 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	DownStream and 3 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	DownStream and 3 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	DownStream and 4 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	DownStream and 4 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	Config DownStream bandplan parameter
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	UpStream band 0 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	UpStream band 0 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	UpStream band 1 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	UpStream band 1 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	UpStream band 2 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	UpStream band 2 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	UpStream band 3 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	UpStream band 3 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	UpStream band 4 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	UpStream band 4 f -l tone
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	Config UpStream bandplan parameter
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	estimated interference
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	Modem Value
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	Flush counter when packet buffer over flow
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	Uplink receive packet counter
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	Uplink send packet counter
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	Clock value parameter
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	Horizontals delay flag
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	Horizontals delay flag history 1
vhdlrCrcVChnUseCbIs	AD (Mbit/s)	Horizontals delay flag history 2

vdsILineDiagSt	ADVANCE	Showtime debug flag
vdsILineDiagSt2	ADVANCE	Showtime debug flag 2
vdsILineDiagSt2D	ADVANCE	Showtime debug flag 2 history
vdsILineDiagSt2DD	ADVANCE	Showtime debug flag 2 history 2
vdsILineDiagStD	ADVANCE	Showtime debug flag history
vdsILineDiagStDD	ADVANCE	Showtime debug flag history2
vdsILineDiagTrn	ADVANCE	Training debug flag
vdsILineDiagTrnD	ADVANCE	Training debug flag history
vdsILineDiagTrnDD	ADVANCE	Training debug flag history 2
VdsILineFgndCntr	ADVANCE	Foreground counter
vdsILinePauseFrmCntr	ADVANCE	802.3 pause frame counter
vdsILineTime250ms	ADVANCE	Other debug flag
vdsILineTime250msD	ADVANCE	Other debug flag history
vdsILineTime250msDD	ADVANCE	Other debug flag history 2
vdsIOtPerfDataESs	ADVANCE	UpStream Error second
vdsIRtPerfDataESs	ADVANCE	DownStream Error second
vdsIVcxoValue	ADVANCE	Clock setup parameter

Change Log:

Version	Date	Description
1.0	2007/11/7	Initial version
1.1	2007/11/27	Add 2.1
1.2	2008/10/15	Add eyebox controller version X3 and X4